

Analog Electronics Lab Viva Questions With Answers

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Analog Electronics Lab Viva Questions

ANALOG ELECTRONICS VIVA & INTERVIEW QUESTIONS PROF. HITESH DHOLAKIYA for computing mathematical functions such as addition, subtraction ,multiplication, integration & differentiation. Q-35 List out the ideal characteristics of OPAMP?
Ans (i) Open loop gain infinite (ii)Input impedance infinite (iii)Output impedance low (iv)Bandwidth infinite

Analog Electronics viva & interview questions

Analog Communication VIVA Questions :- Analog Communication is a data transmitting technique in which information signal is transmitted in analog nature. This always utilizes continuous signals to transmit data which may obtained from audio, image, video etc. An Analog signal is a variable signal continuous in both time and amplitude.

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ANALOG ELECTRONICS Questions :-1. The early effect in a bipolar junction transistor is caused by A. fast turn-on B. fast turn-off C. large collector-base reverse bias D. large emitter-base forward bias. 2. MOSFET can be used as a A. current controlled capacitor B. voltage controlled capacitor C. current controlled inductor D. voltage controlled inductors. 3.

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Electronic Circuit Analysis lab Viva Questions with ...

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LINEAR INTEGRATED CIRCUITS LAB VIVA Questions :-1. What are the advantages of integrated circuits? increased package

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density. Cost reduction . Increased system reliability. Improved functional performance. Increased operating speeds. Reduction in power consumption. 2. What are the popular IC packages available? Metal can package. Dualinlinenpackage.

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In electronics, a sample and hold (S/H, also "follow-and-hold") circuit is an analog device that samples (captures, grabs) the voltage of a continuously varying analog signal and holds (locks, freezes) its value at a constant level for a specified minimum period of time. Question 27. Define Settling Time Of Op Amp? Answer : Op Amp Settling Time.

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MOST EXPECTED VIVA VOICE QUESTIONS FOR BASIC ELECTRONICS LAB PART 1

Analog Electronic Circuits Laboratory Viva Questions 1 Theory_ Questions: 1. The slope of VDS-IDS characteristics of a FET is controlled by a (A) VGS (B) IDS (C) VDS (D) Both (A) & (B) (E) None Answer: [A] VGS 2. What is the condition for compensating the nonlinearity in VDS-IDS characteristics of a FET? VGS Is the gate to source voltage,VDS is ...

Analog Electronic Circuits Laboratory Viva Questions 1 ...

Digital Electronics Interview Questions. A list of top frequently asked Digital Electronics Interview Questions and answers are given below. 1) What is the difference between Latch And Flip-flop? The difference between latches and Flip-flop is that the latches are level triggered and flip-flops are edge triggered.

Top 39 Digital Electronics Interview Questions - javatpoint

Analog Electronic Circuits Laboratory Viva Questions 6. 1. If the emitter bias current increased in a differential amplifier then. 1.

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Input impedance increases 2. CMRR increases 3. The gain of the circuit will increase. 2. Which of the following is not the characteristic of the first stage of the op-amp.

Analog Electronic Circuits Laboratory Viva Questions 6 ...

Answer: An operational amplifier (or an op-amp) is an integrated circuit (IC) that operates as a voltage amplifier. An op-amp has a differential input. That is, it has two inputs of opposite polarity. An op-amp has a single output and a very high gain, which means that the output signal is much higher than input signal.

Power Electronics Lab Viva Questions and Answers ...

Department of Electronics & Communication Engineering Page 9
Viva Questions 1. Define AM and draw its spectrum? 2. Draw the phasor representation of an amplitude modulated wave? 3. Give the significance of modulation index? 4. What are the different degrees of modulation? 5. What are the limitations of square law modulator? 6.

Analog Communications Lab Manual (S/W)

Analog Electronics Objective Questions – Set 1 S Bharadwaj Reddy May 18, 2017 May 4, 2019 Instrumentation Tools assists you with a complete guide of objective questions which mainly targets the aspirants of Electrical, Electronics and Instrumentation engineering Streams to crack the competitive exams and to...

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Power Electronics LAB VIVA Questions :- 1. What is holding current in SCR? It is the minimum current required to hold the SCR in forward conduction state. When the forward current becomes less than holding current, SCR turns from forward conduction state to forward blocking state.

100+ TOP POWER ELECTRONICS LAB VIVA Questions and Answers

Analog Electronic Circuits Lab SSIT - 4 - General Procedure for Calculation :- 1. Input impedance a. Connect a Decade Resistance Box (DRB) between input voltage source and the base of the transistor (series connection). b. Connect ac

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voltmeter (0-100mV) across the biasing resistor R 2.

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ELECTRONICS LABORATORY Viva questions 47-48 7 Appendix-1 49-52 2 Syllabus DIGITAL ELECTRONICS LABORATORY Do not open any irrelevant internet sites on lab ... DIGITAL ELECTRONICS LAB MANUAL DIGITAL ELECTRONICS LAB DO'S DON' TS 1 Be regular to the lab 2 Follow proper Dress Code 3 Maintain Silence 4 Know the theory behind the experiment before ...

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