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## **Lec User Guide**

Cadence® Conformal Smart LEC is the next-generation equivalence checking solution.

Offering key technologies of massive parallelism and adaptive proof, Conformal Smart LEC improves runtime by an average of 4X compared to existing solutions with the same compute resources.

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## **Conformal Smart LEC - Cadence**

Conformal Smart Logic Equivalence Checker is the next-generation equivalency checking solution. With massively parallel architecture and adaptive proof technology, the Conformal Smart LEC delivers dramatic turnaround time improvements in equivalence checking by over 20X for RTL-to-

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## **Conformal Smart LEC - Cadence**

Cadence® Conformal  
® Equivalence Checker  
(EC) makes it possible  
to verify and debug  
multi-million-gate  
designs without using  
test vectors. It offers  
the industry's only  
complete equivalence  
checking solution for  
verifying SoC  
designs—from RTL to  
final LVS netlist

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(SPICE)—as well as  
FPGA designs.

## **Conformal Equivalence Checker - Cadence Design Systems**

Cadence Encounter  
Conformal Equivalence  
Checking User Guide  
(LEC) 3. User -manual-  
cadence Design  
Systems-Encounter  
Conformal Equivalence.  
PDF | In this paper we  
will explore how to use  
the Cadence Conformal



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LEC tool capabilities to verify different types of designs, based on the.

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Cadence Conformal  
LEC Formal Hardware  
Verification ...

CONFORMAL-LEC

Mapping Manager

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Schematics . Title:

Microsoft PowerPoint -

conformal\_debug

Author: NotNaeem

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**Equivalence  
Checking Using  
Cadence Conformal  
LEC**

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capabilities and what  
benefits they provide,  
describing information  
can provide a  
significant saving of

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time and increase in user productivity... on the implementation team is similar to manual ECOs.

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Length : 2 days Digital Badge Available: In this course, you learn to use the Conformal® Equivalence Checker to perform functional verification. You learn the basic flow of

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equivalence checking and how to run hierarchical comparison of designs. The lab exercises follow major topics and are designed to be directly applicable in design and design verification.

### **Conformal Equivalence Checking**

Conformal ECOs at Marvell during pre-mask stage. We have

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done multiple ECOs using Conformal automated flow on various projects for the past few years. Below is the data for

- Example 1 - Block A - Pre-mask ECO was done inside a control block which was instanced multiple(12) times. ECO fix involved combinational logic change in control block.

**Practical ECOs using**

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## **Conformal ECO tool - Cadence**

Cadence ® Conformal  
® Low Power enables  
the creation and  
validation of power  
intent in the context of  
a design. Conformal  
technology combines  
low-power equivalence  
checking with  
structural and  
functional checks to  
enable full-chip  
verification of power-  
efficient designs. ASK  
US A QUESTION

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#### **Conformal Low Power - Cadence**

Cadence Conformal suite of tools contains a tool called Logic Equivalence Checker or LEC. Section 4 of this tutorial describes how to formally verify that the synthesized design is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use Formality for

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equivalence checking.

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**Digital Logic**

**Synthesis and**

**Equivalence**

**Checking Tools ...**

Cadence Design

Systems, Inc.

(NASDAQ: CDNS) today

announced the

Cadence® Conformal®

Smart Logic

Equivalence Checker

(LEC), the next-

generation equivalence

checking solution that

delivers a significant



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improvement in  
equivalence checking  
runtime with minimal  
user effort.

**Cadence Introduces  
the Conformal Smart  
Logic Equivalence ...**

At CDNLive Korea,  
Cadence announced  
the logical equivalence  
checking (LEC) would  
get smart. And not in  
the Maxwell Smart  
sense. It's about 20  
times faster and  
doesn't require manual

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guidance (self-driving LEC). The new product is called Conformal Smart LEC.

**Logical Equivalence  
Checking to Get  
Smart - Cadence  
Community**

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Conformal Equivalence  
Checking User Guide  
(LEC) User-manual-  
cadence Design  
Systems-Encounter  
Conformal Equivalence  
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Mallikarjuna

Ankinapalli, worked at Intel. Answered Feb 5, 2017 · Author has 205 answers and 303k answer views. If you have access to cadence online documentation weblink, then you should be able to access encounter and conformal lec in documentation section :

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benefits they provide,  
describing information  
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